



US005235635A

United States Patent [19] Gulick

[11] Patent Number: **5,235,635**
[45] Date of Patent: **Aug. 10, 1993**

- [54] **KEYPAD MONITOR WITH KEYPAD ACTIVITY-BASED ACTIVATION**
- [75] Inventor: Dale E. Gulick, Austin, Tex.
- [73] Assignee: Advanced Micro Devices, Inc., Sunnyvale, Calif.
- [21] Appl. No.: 589,090
- [22] Filed: Sep. 27, 1990
- [51] Int. Cl.⁵ H04M 1/274
- [52] U.S. Cl. 379/368; 379/365; 379/364; 379/362
- [58] Field of Search 341/22-24, 341/25, 26; 379/359, 360, 361, 362, 364, 365, 368

4,489,310 12/1984 Trotyanetsky 341/24
 4,825,464 4/1989 Wen 379/368
 4,929,944 5/1990 Nakajima 341/24

Primary Examiner—James L. Dwyer
 Assistant Examiner—M. Shehata
 Attorney, Agent, or Firm—Foley & Lardner

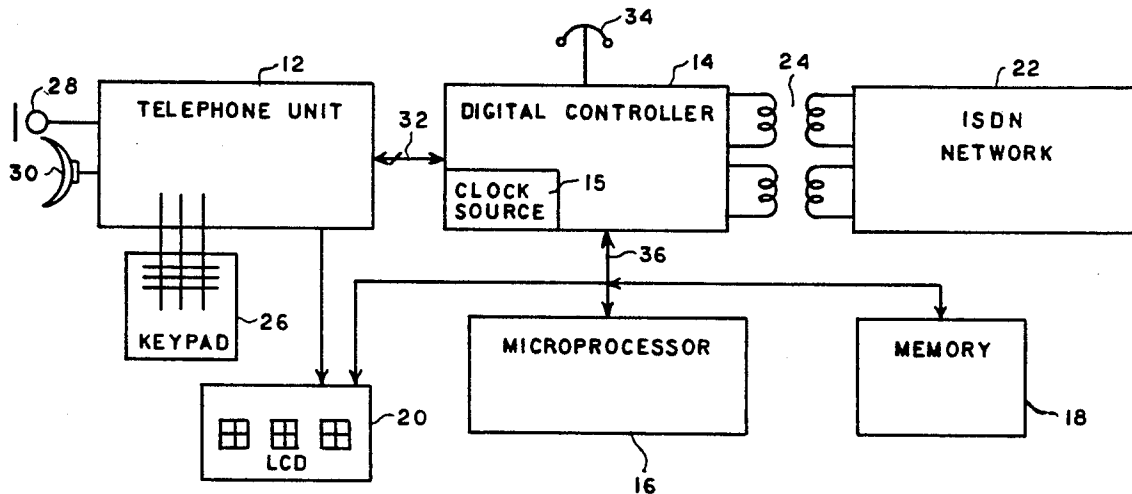
[57] ABSTRACT

A keypad monitor which monitors the condition of a plurality of switches of a keypad is activated in response to receiving clock signals from an external clock source and is deactivated in the absence of the clock signals. The keypad monitor includes a key switch monitor which detects the closure of any one of the keypad switches to provide a first output signal, a clock detector for detecting the absence of the clock signals to provide a second output signal and any key down output logic which is responsive to the first and second output signals for providing an activation signal. The activation signal causes the external clock source to provide the keypad monitor with the clock signals for activating the keypad monitor.

[56] References Cited U.S. PATENT DOCUMENTS

3,988,550	10/1976	Ts'ao	379/364
4,012,601	3/1977	Kehoan	379/364
4,087,638	5/1978	Hayes et al.	341/24
4,324,954	4/1982	Taylor	379/364
4,346,369	8/1982	Macy	341/24
4,374,308	2/1983	Holesha	379/364
4,392,024	7/1983	Shinoi et al.	379/364

18 Claims, 6 Drawing Sheets



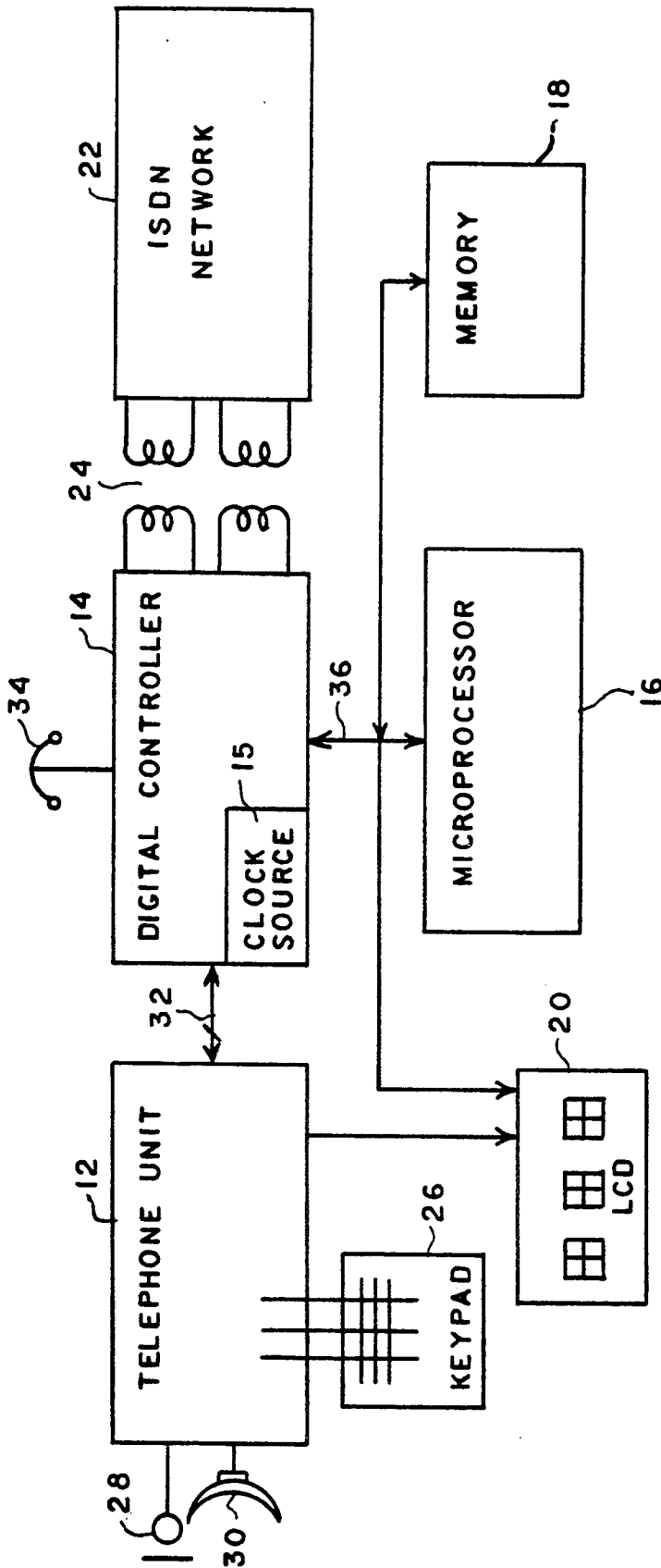


FIG. 1

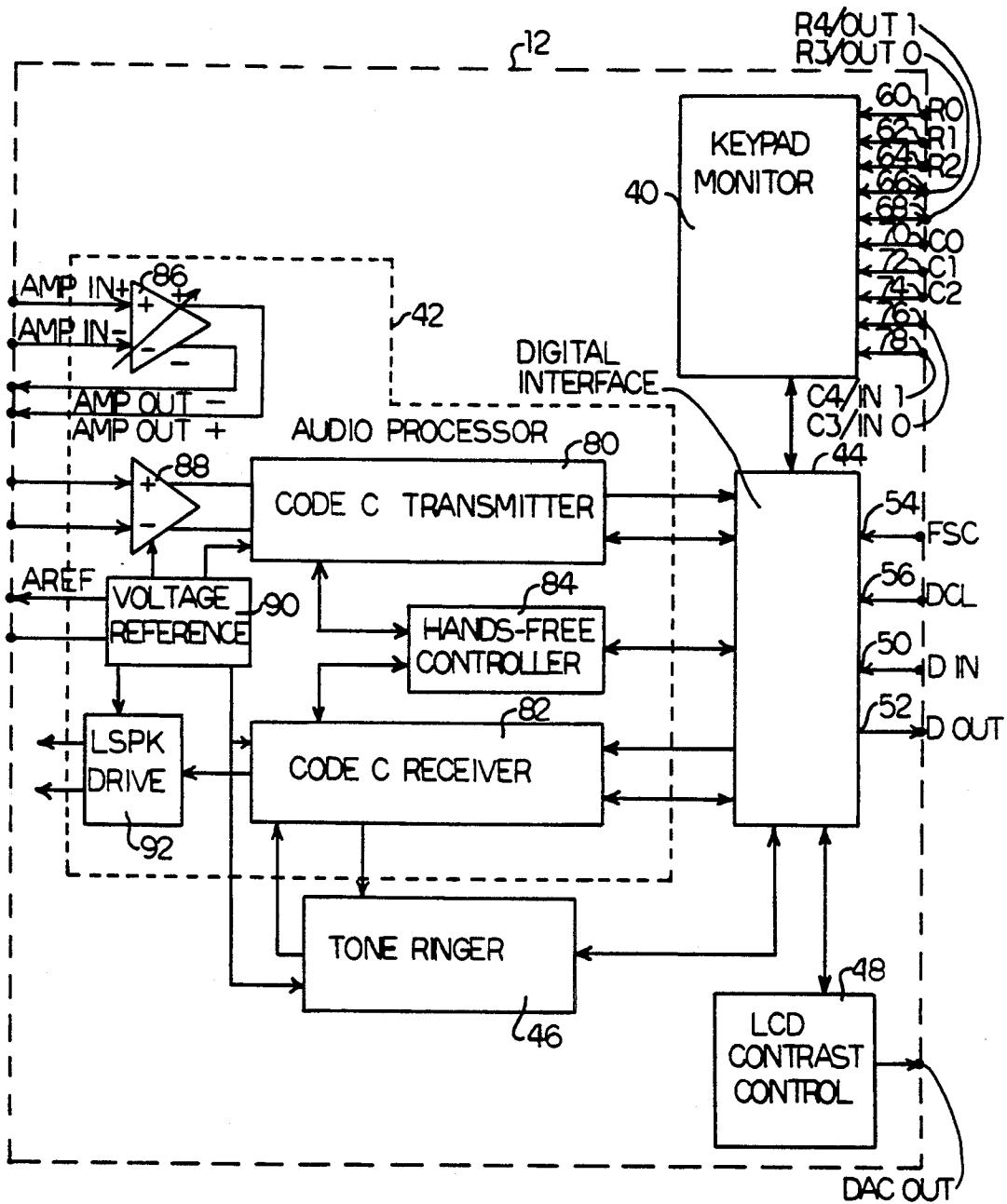


FIG. 2

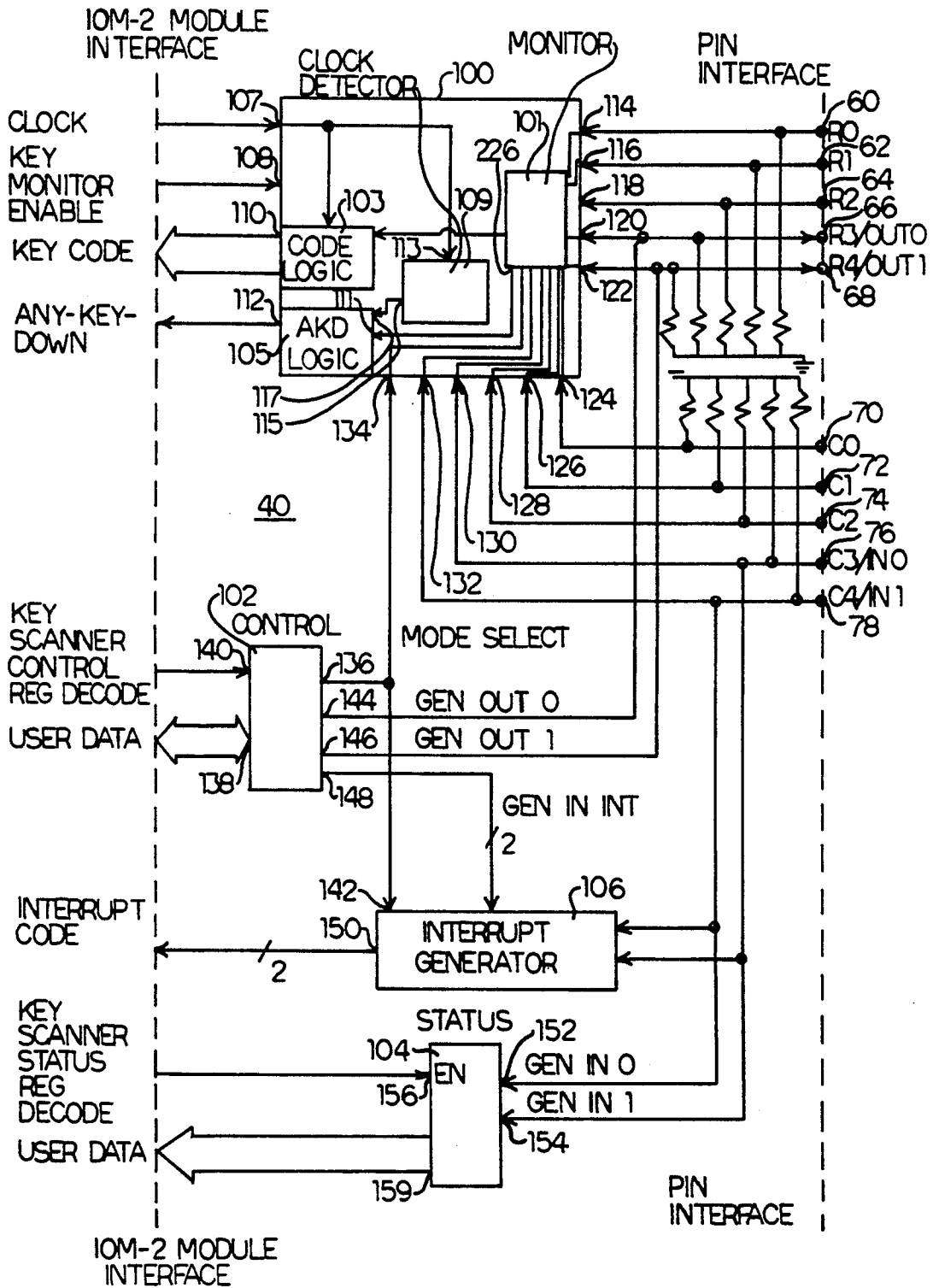


FIG. 3

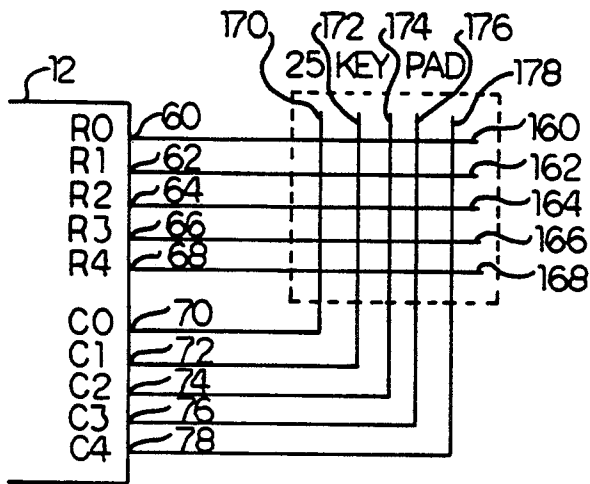


FIG. 4

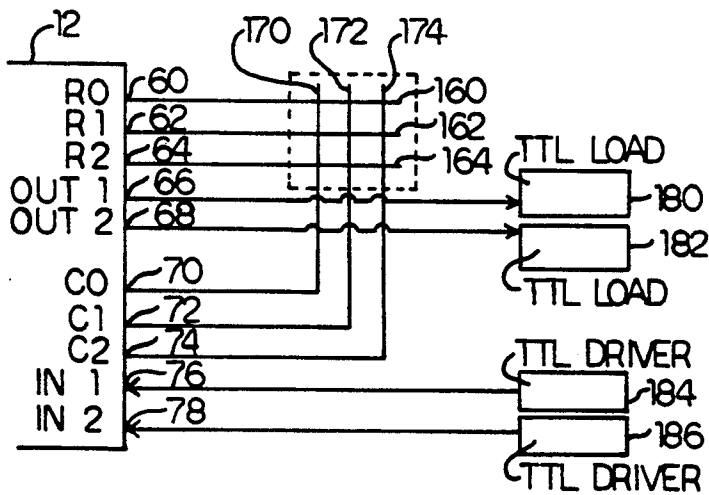


FIG. 5

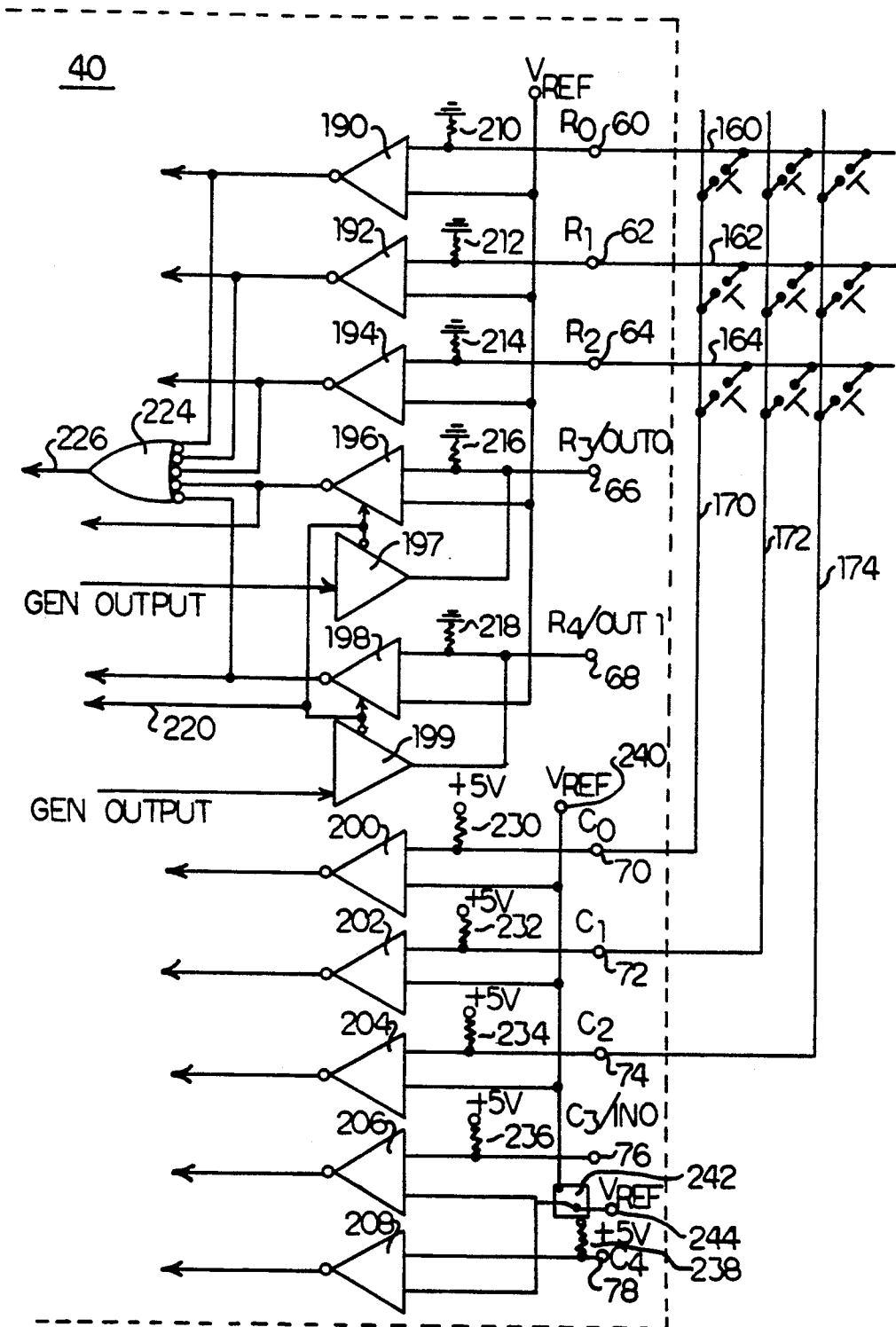


FIG. 6

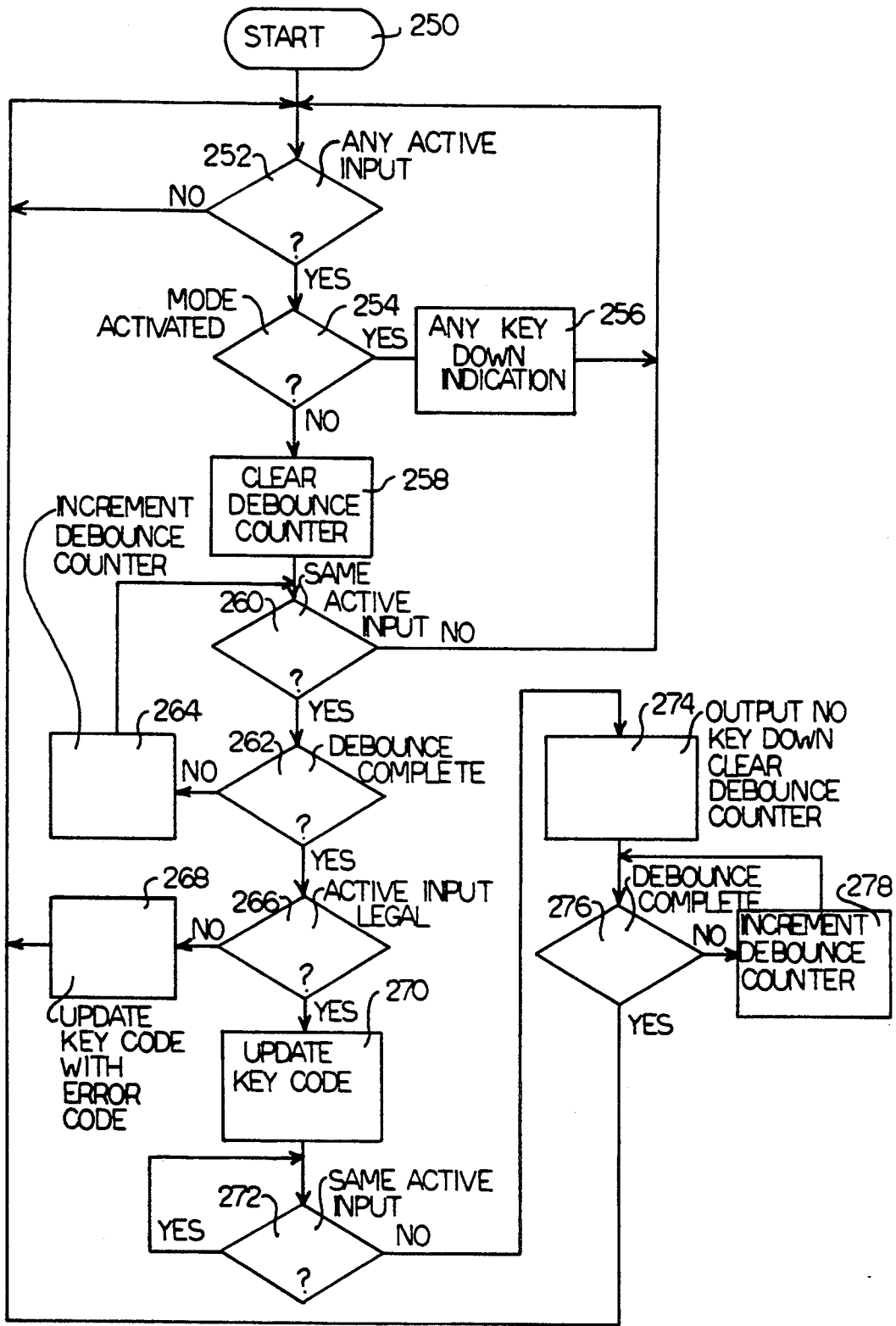


FIG. 7

KEYPAD MONITOR WITH KEYPAD ACTIVITY-BASED ACTIVATION

BACKGROUND OF THE INVENTION

The present invention generally relates to a keypad monitor and more particularly to a keypad monitor which is activated responsive to keypad activity. The present invention still more particularly relates to such a keypad monitor wherein the keypad monitor and associated circuitry is activated in response to received clock signals from an external clock source and deactivated in the absence of clock signals. The keypad monitor of the present invention as disclosed herein may be used to advantage in a hands-free, multiple feature telephone unit adapted for use in an Integrated Services Digital Network (ISDN).

Keypads and monitors associated therewith are well known in the art. Such keypads generally include an array of switches with each switch coupled between a respective different pair of first and second conductors. When a keypad switch is depressed or closed, a condition can then be sensed across the first and second conductors to which a particular switch is coupled to identify which switch is being depressed. Such condition sensing is performed by the keypad monitor.

Keypad monitors for identifying a particular keypad switch being closed have been implemented in integrated circuit form. The keypad monitors of such integrated circuits generally include a first plurality of inputs arranged to be coupled to the first keypad conductors and a second plurality of inputs arranged to be coupled to the second keypad conductors. The keypad monitors further generally include internal circuitry which scan the first and second plural inputs for detecting the closure of a keypad switch.

One dominant application for keypads and keypad monitors is in telephone equipment. Keypads are used in such applications for dialing telephone numbers or selecting various features provided by a particular piece of telephone equipment. Such features may include call transfer, call conference, hold, or multiple-line selection, for example.

The scanning circuitry of keypad monitors is generally responsive to clock signals applied to the keypad monitor integrated circuit from a clock source external to the keypad monitor integrated circuit. The keypad monitor integrated circuit may also include other circuitry which require the clock signals for operation.

In the telephone industry, there is a strict requirement that telephone equipment which is not in use, and thus is in an idle state, consume a limited amount of power. Hence, such equipment must include a means for deactivating the equipment when not in use and activating the equipment when it is placed into service. In limited feature telephone equipment, this is generally accomplished by activating the telephone circuitry responsive to the telephone switch being placed off-hook by a user. In such a case, when the telephone is placed off-hook, activating power together with the externally derived clock signals are applied to the telephone equipment circuitry including the keypad and keypad monitor.

While the foregoing is generally successful in limited feature telephone equipment, it is not always suitable in multi-featured telephone equipment and especially in such equipment which is under the control of a microprocessor. This obtains because a user may select various telephone features in advance to taking the tele-

phone equipment off-hook. In such a case, the keypad monitor, the microprocessor, and any other interface circuitry associated with the keypad monitor and feature selection would not be activated during such feature preselection prior to taking the telephone equipment off-hook.

SUMMARY OF THE INVENTION

The invention therefore provides a system for activating and use in a telephone apparatus of the type associated with a keypad including a plurality of keypad switches and which includes internal circuitry which is deactivated when in an idle state for conserving power and activated in response to receiving clock signals from an external clock source. The system for activating the telephone apparatus includes keypad monitoring means coupled to the keypad switches for monitoring the condition of the keypad switches and providing a first output signal in response to a change in the condition of any one of said keypad switches, clock detecting means for detecting the presence and absence of the clock signals and providing a second output signal responsive to detecting the absence of the clock signals, and output means coupled to the external clock source and responsive to the first and second output signals for providing an activation signal to cause the external clock source to provide the telephone apparatus with the clock signals.

The invention further provides a keypad monitor of the type which monitors the condition of a plurality of switches of a keypad, wherein the keypad monitor is activated in response to receiving clock signals from an external clock source and deactivated in the absence of the clock signals. The keypad monitor includes keypad monitoring means coupled to the keypad switches for monitoring the condition of the keypad switches and providing a first output signal in response to detecting a change in the condition of any one of the keypad switches, clock detecting means for detecting the presence and absence of the clock signals and providing a second output signal responsive to detecting the absence of the clock signal, and output means coupled to the external clock source and responsive to the first and second output signals for providing an activation signal to cause the external clock source to provide the keypad monitor with the clock signals for activating the keypad monitor.

BRIEF DESCRIPTION OF THE DRAWINGS

The features of the present invention which are believed to be novel are set forth with particularity in the appended claims. The invention, together with further objects and advantages thereof, may best be understood by making reference to the following description taken in conjunction with the accompanying drawings in the several figures of which like reference numerals identify identical elements and wherein:

FIG. 1 is a block diagram of an ISDN telephone system including an ISDN hands-free, multiple feature telephone unit embodying the present invention;

FIG. 2 is a block diagram of the hands-free, multiple feature telephone unit of FIG. 1 embodying the present invention;

FIG. 3 is a block diagram of a keypad monitor embodying the present invention incorporated into the hands-free, multiple feature telephone unit of FIG. 2;

FIG. 4 is a simplified block diagram illustrating the keypad monitor of FIG. 3 when operating in a first mode;

FIG. 5 is a simplified block diagram illustrating the keypad monitor of FIG. 3 when operating in a second mode;

FIG. 6 is a schematic circuit diagram illustrating the circuitry of the keypad monitor of FIG. 3 which may be used for sensing the closure of any one of the keypad switches and providing an output signal responsive thereto for use in activating the keypad monitor and circuitry for identifying a particular keypad switch being closed; and

FIG. 7 is a flow diagram illustrating the manner in which the keypad monitor of FIG. 3 may be implemented for monitoring a plurality of keypad switches to practice the present invention in accordance with a preferred embodiment.

DESCRIPTION OF THE PREFERRED EMBODIMENT

Referring now to FIG. 1, it illustrates in block diagram form, an ISDN telephone system 10 embodying the present invention. The system 10 generally includes a hands-free, multiple feature telephone unit 12, a digital controller 14, a microprocessor 16, a memory 18, and a liquid crystal display unit 20. The system 10 is transformer-coupled to an ISDN network 22 by a transformer 24.

The telephone unit 12 is coupled to a keypad 26 in a manner to be described in greater detail hereinafter. To that end, as will also be seen hereinafter, the unit 12 includes a keypad monitor embodying the present invention for monitoring the keypad switches of the keypad to first detect when any one of the keypad switches is closed to obtain clock signals from an external clock source and then to identify a particular key being depressed. Depression of the keys of the keypad 26 may be made for dialing a telephone number or for selecting a particular feature of the telephone unit 12. Also coupled to the unit 12 is a microphone 28 which serves to convert audible speech to analog voltage form and a loud-speaker 30 which serves to convert analog voltages representative of human speech to audible sound. The microphone 28 and speaker 30 may, of course, be contained in the same housing as the telephone unit 12. As will also be seen hereinafter, the telephone unit 12 includes an audio processor which converts the analog voltages produced by the microphone 28 representative of audible speech to output digital signals and which also converts input digital signals representative of human speech to analog voltages for conversion to audible sound by the speaker 30.

The telephone unit 12 is coupled to the digital controller 14 by a four-conductor bus 32. The bus 32 is preferably of the type well known in the art as an IOM TM-2 bus (IOM is a trademark of Siemens AG). The bus will be described in greater detail with respect to FIG. 2. It basically provides a serial digital interface between the telephone unit 12 and the digital controller 14.

The digital controller 14 provides a digital interface between the telephone unit 12 and the bus 32 to the ISDN network 22 and the microprocessor 16. The digital controller is preferably the Am79C30A Digital Subscriber Controller available from Advanced Micro Devices, Inc., the assignee of the present invention, and is well known in the art. It provides, in addition to the

digital interface function, a CODEC transceiver function to support the use of a standard telephone handset 34 coupled thereto to enable the telephone user to communicate over the ISDN network 22 from the standard handset 34. It also includes a clock source 15 for providing clock signals to the telephone unit 12 in response to receiving an activation signal from the telephone unit 12.

The microprocessor 16 is coupled to the digital controller 14 over a microprocessor bus 36 which also couples the microprocessor to the memory 18 and the liquid crystal display unit 20. The microprocessor 16 executes programs stored in the memory 18 and thus provides the intelligence of the system 10. The microprocessor 16 allows the telephone system 10 to perform the functions customarily performed by such systems. It also responds to requests for service by the telephone user when the telephone user takes the telephone handset 34 off-hook or when the telephone user activates the telephone unit 12 in a manner to be described hereinafter by depressing one of the keys of the keypad 26. Such microprocessors and the programs which they execute are also well known in the art.

The liquid crystal display unit 20 is coupled to the telephone unit 12 and the digital controller 14. It functions to provide a display of, for example, a telephone number being dialed, the telephone number of an incoming call or an indication of various telephone unit features selected by the user such as hands-free, hold, speaker mute, et cetera.

Referring now to FIG. 2, it illustrates the telephone unit 12 in greater detail. The telephone unit 12 generally includes the keypad monitor 40 embodying the present invention, an audio processor 42, a digital interface 44, a tone ringer 46, and a liquid crystal display contrast control 48. The functional units of the telephone unit 12 are all integrated into a single integrated circuit chip to form the telephone unit 12.

The digital interface 44 provides a serial digital interface between the functional modules of the telephone unit 12 and the digital controller 14 over the four-conductor bus 32. The digital interface 44 includes an input 50 for receiving serial digital data from the digital controller 14 and an output 52 for conveying serial digital data to the digital controller 14. The digital data is conveyed between digital interface 44 of the telephone unit 12 and the digital controller 14 using a multiplexed channel comprising three repetitively conveyed thirty-two bit frames. The frame structure is well known in the art. Each frame comprises a plurality of channels which include serial digital representations of received or transmitted speech, data to be entered into register, data to be read from register, register addresses, keypad information such as a six-bit indication of a particular key being depressed, and other control information.

To provide synchronization for the data transfer, the digital interface 44 includes another input 54 (FSC) for receiving frame synchronizing signals at an 8 kilohertz rate from the digital controller 14 and an input 56 (DCL) for receiving clock signals at a 1.536 megahertz rate from the digital controller 14 for synchronizing the transmission and the reception of the individual bits of data and for causing the keypad monitor 40 to scan its inputs to identify a closed keypad switch.

In keeping with strict power requirements for such telephone units when not in use, the telephone unit 12 is in a deactivated state when not in use for conserving power. When no data is being transferred between the

telephone unit 12 and the digital controller 14 or when the telephone unit 12 is otherwise deactivated, the data output 52 is held high. As will be seen hereinafter, the telephone unit 12 is activated when any one keypad switch of the keypad is depressed or closed causing the keypad monitor 40 to provide an activation signal referred to as an any key down indication. This causes the data output 52 to be pulled low to in turn cause the digital controller 14 to provide the aforementioned clock signals from its clock source 15. Upon receipt of the clock signals, the telephone unit 12 is activated permitting the keypad monitor to scan its input terminals to determine which key is depressed. Hence, at any time data is to be transferred from the keypad 26 to the digital controller 14, the data output 52 is momentarily pulled low to cause the digital controller to provide the clock signals from its clock source 15.

The keypad monitor 40 embodying the present invention includes a first plurality of inputs coupled to input terminal pins 60, 62, 64, 66 and 68 which are arranged to be coupled to a first plurality of keypad switch conductors (row conductors) and a second plurality of inputs coupled to input terminal pins 70, 72, 74, 76 and 78 which are arranged to be coupled to a second plurality of keypad switch conductors (column conductors). Since there are five inputs for row conductors and five inputs for column conductors, the keypad monitor, when operative in a first mode, is capable of monitoring a first set of input terminals including all of the input terminals and thus, is capable of monitoring a full-sized keypad comprising twenty-five keypad switches.

When the keypad monitor 40 is operative in a second mode, the keypad monitor monitors a second set of input terminals, namely, input terminals 60, 62, 64, 70, 72 and 74. Hence, in the second mode, there are three row inputs and three column inputs for monitoring a keypad comprising nine keypad switches. Also, when the keypad monitor is in the second mode, terminal pins 66 and 68 are converted to general outputs, not associated with keypad monitoring, and terminal pins 76 and 78 are converted to general inputs, also not associated with keypad monitoring. As a result, when in the second mode, a third set of terminal pins comprising terminal pins 66, 68, 76, and 78 are not needed for keypad monitoring but are converted to other uses to make full utilization of all of the integrated circuit terminal pins.

Each keypad switch is coupled between a respective different pair of row and column conductors. When a keypad switch is depressed or closed, the keypad monitor 40, after providing the aforementioned any key down indication and receiving the clock signals from the digital controller 14, then scans the input terminals and senses for a condition which indicates which keypad switch is closed. After detecting which keypad switch is closed, the monitor pauses to account for key bounce and then generates a 6-bit code representative of the keypad switch which is closed. The 6-bit code is conveyed to the digital interface 44 which then places the code into its proper channel and conveys the code to the digital controller 14 across the four-conductor bus 32 from the digital interface output 52.

The audio processor 42 includes a CODEC transmitter 80, a CODEC receiver 82, a hands-free controller 84, a variable gain amplifier 86, a fixed gain amplifier 88, a voltage reference 90, and a loudspeaker drive 92. The audio processor 42 thus provides all of the audio processing within the telephone unit 12.

The CODEC transmitter 80 receives analog voltages representative of audible speech from the fixed gain amplifier 88. The fixed gain amplifier 88 includes a pair of inputs which may be directly coupled to an external microphone or coupled to the external microphone through the variable gain amplifier 86. Preferably, the microphone is capacitively coupled to the telephone unit 12 at the inputs of either the variable gain amplifier 86 or the fixed gain amplifier 88. If the variable gain amplifier 86 is utilized, its outputs should preferably be capacitively coupled to the inputs of the fixed gain amplifier 88 through external capacitors.

The CODEC transmitter 80 after receiving the audio voltages representative of human speech, digitizes the analog voltages into an 8-bit digital word and conveys the same to the digital interface 44. The digital interface 44 then serializes the digital word received from the CODEC transmitter and places the same into the appropriate channel for transmission to the digital controller from the output 52.

The CODEC receiver 82 includes a digital-to-analog converter which converts the 8-bit digital words received from the digital interface 44 representative of received human speech and converts the same to analog voltages. The received audio is received by the digital interface 44 at input 50 in serial bit format and converts the same to 8-bit parallel bit words for conversion to analog form by the CODEC receiver 82. The CODEC receiver 82 is coupled to the loudspeaker drive 92 which includes an amplifier for amplifying the analog voltages representative of received human speech. The loudspeaker drive 92 is adapted to be coupled to the external speaker 30 (FIG. 1) for reproducing the received audio.

The tone ringer 46 is coupled to the CODEC receiver 82. The tone ringer provides ringing tones in digital format which are also converted to analog form by the CODEC receiver. The analog voltages representative of the ringing tones are then conveyed to the loudspeaker drive 92 for reproduction by the external speaker coupled to the loudspeaker drive 92. The tone ringer 46 is preferably programmable by a register (not shown) contained within the tone ringer 46 to permit a plurality of different distinguishable rings to be produced by the tone ringer 46. The register within the tone ringer is preferably programmed by the microprocessor.

Lastly, the liquid crystal display contrast control 48 provides an output voltage for the liquid crystal display to adjust the contrast thereof. The liquid crystal display contrast control 48 is coupled to the digital interface 44 and is controllable by a register within the digital interface 44 which may be programmed through the 4-conductor bus 32.

The hands-free controller 84 is coupled between the CODEC transmitter 80 and CODEC receiver 82. The hands-free controller 84 controls the enablement of the CODEC transmitter and CODEC receiver to provide hands-free simplex operation of the telephone unit 12. The hands-free controller 84 is coupled to the digital interface 44 for receiving control signals therefrom.

Referring now to FIG. 3, it illustrates the keypad monitor 40 in greater detail. The keypad monitor 40 generally includes keypad monitor logic 100, a control register 102, a status register 104, and an interrupt message generator 106.

The keypad monitor logic 100 includes a clock input 10 for receiving clock signals from the digital interface

44, a key monitor enable input 108 for receiving enable signals from the digital interface 44, an output 110 for providing the digital interface 44 with the 6-bit digital representation of a keypad switch being closed, and another output 112 for providing the digital interface 44 with the aforementioned any key down indication.

The keypad monitor logic further includes a key switch monitor 101, a code logic 103, an any key down (AKD) logic 105, and a clock detector 109. Lastly, the keypad monitor logic includes an input 134 which is coupled to the output 136 of the control register 102 which controls whether the keypad monitor 40 is in the first or second mode of operation. The key switch monitor 101 includes the first plurality of inputs 114, 116, 118, 120, and 122 which are coupled to terminal input pins 60, 62, 64, 66, and 68 respectively. The key switch monitor 101 also includes the second plurality of inputs 124, 126, 128, 130, and 132 which are coupled to the input terminal pins 70, 72, 74, 76, and 78, respectively.

The key switch monitor 101 forms a keypad monitoring means which monitors the condition of the keypad switches for detecting when any one of the keypad switches has closed to generate a first output signal at an output 226 which is used in the generation of the any key down signal. The output 226 is coupled to an input 111 of the any key down logic 105. The clock detector 109 includes an input 113 which is coupled to the input 107 of the keypad monitor logic 100 for receiving clock signals from the clock source 15 of the digital controller 14 through the digital interface 44. The clock detector 109 detects the presence and absence of the clock signals. In the absence of the clock signals, the clock detector provides a second output signal at an output 115 which is coupled to another input 117 of the any key down logic. The any key down logic 105 provides the any key down signal at output 112 responsive to receiving the first output signal at its input 110 and the second output signal at its input 117. As a result, the any key down logic generates the any key down signal at its output 112 when any one of the keypad switches is closed together with the absence of clock signals being received at input 107. The any key down signal provided at output 112 then causes the output 52 of the digital interface 44 to be pulled low momentarily to cause the digital controller to provide the clock signals from its clock source 15.

The clock signals provided by the clock source 15 of the digital controller 14 cause the digital interface 44 to be activated and also causes the key switch monitor 101 to scan its inputs to identify which one of the keypad keys is closed. The logic levels resulting from the scanning of the key switch monitor inputs are conveyed to the code logic 103 which then generates the six-bit key code indicating which one of the keypad switches is closed. The generation of the six-bit key code is performed in a manner well known in the art.

The control register 102 includes an input 138 for receiving programming data from the digital interface 44. The control register includes another input 140 for receiving the address of the control register to enable the control register to be properly programmed to set the keypad monitor into the first or second mode of operation. For example, when the first mode of operation is selected, the output 136 will provide a high level to input 134 of the keypad monitor logic and a high level to an input 142 of the interrupt generator 106. When the keypad monitor is to operate in the second mode, the control register 102 will provide at output

136 a low level to input 134 of the keypad monitor logic and to the input 142 of the interrupt generator 106.

The control register 10 includes another output 144 coupled to input 120 of the keypad monitor logic and an output 146 coupled to input 122 of the keypad monitor logic. These outputs, when the keypad monitor is operative in the second mode, provide a high logic level to the inputs 120 and 122 when these outputs are used for general outputs and thus enable the inputs 120 and 122 to be used as general outputs. In other words, when the keypad monitor 40 is to be used in the second mode and terminal pins 66 and 68 are converted for general output purposes, the inputs 120 and 122 are provided with a high TTL voltage level.

The control register includes a further output 148 which is coupled to the interrupt generator 106. The output 148 allows the interrupt generator 106 to be enabled when the keypad monitor is in the second mode of operation to permit the interrupt generator 106 to generate interrupt messages responsive to the input conditions at pins 76 and 78. If during the second mode of operation the input pins 76 and 78 are to be used for purposes other than to generate interrupt messages, the interrupt generator 106 would not be enabled by the output 148. The interrupt generator 106 has an output 150 for providing the generated interrupt messages. The output 150 is coupled to the digital interface 44 to convey the interrupt messages to the digital controller 14 and the microprocessor 16.

The status register 104 includes a pair of inputs 152 and 154 coupled to terminals 76 and 78 respectively. The status generator 104 also includes an input 156 for receiving its address and enabling the status register to provide status information as to the condition of pins 76 and 78 over an output 159. The output 159 is coupled to the digital interface 44 so that the status of the status register 104 may be read by the microprocessor 16.

Referring now to FIG. 4, it illustrates the manner in which the keypad monitor of the telephone unit 12 is coupled to a keypad comprising twenty-five keypad switches when operative in the first mode of operation. As will be noted in FIG. 4, each of the row input terminal pins 60, 62, 64, 66, and 68 is coupled to a row conductor 160, 162, 164, 166, and 168 respectively. Similarly, each column terminal input pin 70, 72, 74, 76, and 78 is coupled to a respective one of the column conductors 170, 172, 174, 176, and 178. As configured in this manner, the keypad monitor of the telephone unit 12 is capable of supporting a keypad having twenty-five keypad switches.

Referring now to FIG. 5, it illustrates the manner in which the keypad monitor of the telephone unit 12 is coupled to a keypad comprising nine keypad switches when operative in the second mode of operation. It will be noted that in FIG. 5, each of the row terminal input pins 60, 62, and 64, is coupled to a respective one of the row conductors 160, 162, and 164. Similarly, each of the column input terminal pins 70, 72, and 74 is coupled to a respective one of the column conductors 170, 172, and 174. The terminal pins 66 and 68 are converted for general output purposes and are coupled to TTL loads 180 and 182. Pins 76 and 78 are coupled to TTL drivers 184 and 186 and thus serve as general purpose input pins. Hence, when the keypad monitor of the present invention is rendered operative in the second mode, the third set of terminal pins including terminal pins 66 and 68 are converted for general output purposes, and terminal pins 76 and 78 are converted for general input

purposes, not associated with the monitoring of the keypad.

Referring now to FIG. 6, it illustrates in greater detail the key switch monitor 101. The key switch monitor 101 is shown operative in the second mode of operation. The key switch monitor includes a plurality of sense amplifiers 190, 192, 194, 196, 198, 200, 202, 204, 206, and 208. The sense amplifiers 190, 192, and 194 include a first input coupled to the row conductors 160, 162, and 164 respectively and to ground potential through internal pull down resistors 210, 212, and 214 respectively. Sense amplifiers 196 and 198 also have a first input coupled to terminal pins 66 and 68 respectively and to ground potential through pull down resistors 216 and 218, respectively. However, it will be noted, that terminal pins 66 and 68 are not coupled to any row conductors.

Associated with sense amplifiers 196 and 198 are sense amplifiers 197 and 199 respectively. Sense amplifiers 197 and 199 each include an input which is coupled to a general purpose output for receiving a general purpose output signal. When the keypad monitor is rendered in the second mode, a logical zero is impressed upon mode select line 220 which is coupled to the output 136 of the control register 102 (FIG. 3) which disables sense amplifiers 196 and 198 and enables sense amplifiers 197 and 199. As a result, the terminal pins 66 and 68 are converted to general output pins for driving TTL loads 180 and 182 (FIG. 5).

It will also be noted that each of the sense amplifiers 190, 192, 194, 196, and 198 includes a second input which is coupled to a reference voltage at a terminal 222. The reference voltage is selected such that when one of the keypad switches is depressed or closed, a logical one is provided at the output of the sense amplifier coupled to the row of the switch which has been depressed. It will also be noted that the outputs of the sense amplifiers are all coupled to an input of an OR gate 224. Hence, when one of the key switches of the keypad is depressed, OR gate 224 will provide at an output 226 the first output signal previously described.

Sense amplifiers 200, 202, and 204 each include a first input which is coupled to one of the keypad column conductors 170, 172, and 174 and to a +5 volts through internal pull up resistors 230, 232, and 234. Sense amplifiers 206 and 208 also include a first input which is coupled to a +5 volts through an internal pull up resistor 236 and 238 and are also coupled to the column input terminal pins 76 and 78 which are not coupled to any column lines. Sense amplifiers 200, 202, and 204 have a second input coupled to a voltage reference at a terminal 240 which is equal to the reference voltage supplied at terminal 222. When a keypad switch is depressed, the sense amplifier coupled to the column line which is in turn coupled to the closed keypad switch will provide a logical one output. A logical one output of one of the column sense amplifiers 200, 202, and 204 and the logical one output provided by one of the row sense amplifiers 190, 192, or 194 are sensed by the key switch monitor as it scans and are used by the code logic 103 to generate the unique 6-bit code indicating which keypad switch is closed in a manner well-known in the art.

It will also be noted that sense amplifiers 206 and 208 include a second input coupled to a switch 242 which may be a solid state switch. One pole of the switch is coupled to the reference potential terminal 240 and the other pole of the switch is coupled to another reference

potential terminal 244. When the keypad monitor is operative in the second mode, the low logic level on the mode select line 220 causes the switch 242 to select the reference potential at terminal 244. By doing so, the second inputs of the sense amplifiers 206 and 208 are coupled to a different reference potential for sensing high or low TTL levels at their respective first inputs coupled to terminals 76 and 78. In this manner, the terminals 76 and 78 are converted for general input purposes and are coupled to TTL drivers 184 and 186 as illustrated in FIG. 5.

Referring now to FIG. 7, it illustrates the manner in which the state control 115 may be implemented for controlling the operation of the keypad monitor 40. The keypad monitor begins at start in step 250. It first determines in step 252 if any one of the keypad switches has been depressed. If not, the keypad monitor will return to start. If a keypad switch has been depressed, the keypad monitor then in step 254 determines if it is in the deactivated mode. It performs this step by the clock detector detecting the absence of the clock signals from the digital controller 14. If the keypad monitor is deactivated, then in step 256, it provides the any key down indication to be conveyed to the digital interface 44. This causes the digital controller 14 to provide clock pulses from its clock source 15 to the telephone unit 12 over the bus 32. The digital interface 44 and keypad monitor 40 are then activated and the keypad monitor then returns to start.

If the key is still depressed, the keypad monitor will sense that the key is depressed in step 252, and then determine in step 254 that it is not in the deactivated mode. Since the keypad monitor 40 is now activated, the key switch monitor 101 will now receive clock signals causing the encoders to scan their inputs.

The keypad monitor then clears the debounce counter in step 258. The debounce counter is provided to provide a delay time for the keypad switch being depressed to be stabilized and to dissipate any transients that may be occurring within the keypad.

The keypad monitor then in step 26 determines if the keypad switch currently depressed is the same active input as originally detected. If not, the keypad monitor returns to start. If it is the same active input, the keypad monitor then determines in step 262 if the debounce period has been completed. If not, the keypad monitor in step 264 increments the debounce counter and then returns to step 260. When the debounce counter is incremented a sufficient number of times to indicate that the debounce time period has been completed, the keypad monitor will detect in step 262 that the debounce time period has been completed and then proceeds to step 266 to determine if the active input is a legal input. If the active input is not legal, caused for example by two keys being simultaneously depressed, the keypad monitor, through the state control 115, updates the keycode with the predetermined error key code in step 268. After providing the error key code, the keypad monitor returns to start.

If the active input is a legal input as determined in step 266, the keypad monitor proceeds to step 270 to enable the encoders 105 and 109 to update the key code. When updating the key code, the keypad monitor provides the six-bit key code of the particular key being depressed by providing the parallel three-bit row and column addresses.

After updating the key code, the keypad monitor then determines if the same key is still being depressed

in step 272. The keypad monitor continuously repeats step 272 until the same key is no longer being depressed. When the key is no longer being depressed, the keypad monitor will output a six-bit code indicating that no key is being depressed. This no key down output is a predetermined six-bit code, and will clear the debounce counter in step 274. The keypad monitor then in step 276 determines if the debounce time period has completed. If not, the keypad monitor in step 278 increments the debounce counter and returns to step 276 to determine if the debounce time period has completed. Once the debounce time period has completed, the keypad monitor returns to start to detect the depression of another keypad switch.

Hence, from the foregoing, it can be seen that the keypad monitor provides a 6-bit code indicating that no key is down, a 6-bit code indicating an error when the input is illegal, a 6-bit code indicating which keypad switch is depressed, and a separate indication of an any key down condition to cause the digital controller to provide clock signals to activate the interface 44 and the keypad monitor 40.

After receiving the clock signals from the digital controller 14, the keypad monitor then determines which key is depressed as described above. Before providing the 6-bit code indicating the particular key being depressed, the keypad monitor provides a debounce time period and after the keypad switch is opened, the keypad monitor provides another debounce time period. If two keys are being depressed simultaneously, the keypad monitor generates a 6-bit error code to inform the microprocessor 16 that it should disregard the keypad switch closure. Also, when no key is depressed, the keypad monitor provides a 6-bit no key down code to the interface 44.

While a particular embodiment of the present invention has been shown and described, modifications may be made, and it is therefore intended to cover in the appended claims all such changes and modifications which fall within the true spirit and scope of the invention.

What is claimed is:

1. In a telephone apparatus having a keypad including a plurality of keypad switches and which includes internal circuitry which is deactivated when in an idle state for conserving power and activated in response to receiving clock signals from an external clock source, a system for activating said telephone apparatus comprising:

keypad monitoring means coupled to said plurality of keypad switches for monitoring the condition of said plurality of keypad switches and providing a first output signal in response to a change in the condition of any one of said plurality of keypad switches;

clock detecting means for detecting the presence and absence of said clock signals and providing a second output signal responsive to detecting the absence of said clock signals; and

output means coupled to said external clock source and responsive to said first and second output signals for providing an activation signal to cause said external clock source to provide said telephone apparatus with said clock signals.

2. A system as define in claim 1 wherein said keypad monitoring means is arranged to provide said first output signal in response to said change in condition of any one of said keypad switches.

3. A system as defined in claim 1 wherein said keypad monitoring means further includes a first and a second plurality of conductors and wherein each of said keypad switches is arranged between a respective different pair of said first and said second conductors, wherein said keypad monitoring means includes a first plurality of inputs coupled to said first plurality of conductors and a second plurality of inputs coupled to said second plurality of conductors, and wherein said keypad monitoring means is arranged to monitor only said first or said second plurality of inputs for providing said first output signal.

4. A system as defined in claim 3 wherein said first plurality of conductors comprises row conductors coupled to said first plurality of inputs, wherein said second plurality of conductors comprises column conductors coupled to said second plurality of inputs, and wherein said keypad monitoring means is arranged to monitor only said first plurality of inputs for providing said first output signal.

5. A system as defined in claim 4 wherein said keypad monitoring means comprises an OR gate having a plurality of inputs coupled to said first plurality of inputs of said keypad monitoring means and having an output coupled to said output means for providing said output means with said first output signal.

6. A system as defined in claim 2 wherein said keypad monitoring means is further arranged for providing a unique multiple-bit code in response to said change in condition of any one of said keypad switches and wherein said output means is arranged for providing said activation signal independently of said unique multiple-bit code.

7. A system as defined in claim 6 further including digital interface means for serializing said multiple-bit code and for coupling said output means to said external clock source independently from said serialized multiple-bit code.

8. A system as defined in claim 7 further including a digital controller coupled to said digital interface means and wherein said digital controller includes said external clock source and is arranged for receiving said serialized multiple-bit code from said digital interface means.

9. A system as defined in claim 8 wherein said digital interface means includes an output coupled to said digital controller, wherein said digital interface means is arranged to provide from said output to said digital controller a multiplexed output having multiple channels, wherein said digital interface means is arranged to convey said multiple-bit code within one of said channels and wherein said digital interface means is arranged to set said output to a momentary predetermined voltage level responsive to said activation signal for conveying said activation signal to said digital controller.

10. A system as defined in claim 6 wherein said multiple-bit code is a six-bit code.

11. A system as defined in claim 6 wherein said keypad monitor means are arranged to provide said multiple-bit code in response to receiving said clock signals from said external clock source.

12. A keypad monitor which monitors the condition of a plurality of switches of a keypad, wherein said keypad monitor is activated in response to receiving clock signals from an external clock source and deactivated in the absence of said clock signals, said keypad monitor comprising:

13

14

keypad monitoring means coupled to said switches for monitoring the condition of said plurality of switches and providing a first output signal in response to detecting a change in the condition of any one of said plurality of switches;

clock detecting means for detecting the presence and absence of said clock signals and providing a second output signal responsive to detecting the absence of said clock signals; and

output means coupled to said external clock source and responsive to said first and second output signals for providing an activation signal to cause said external clock source to provide said keypad monitor with said clock signals for activating said keypad monitor.

13. A keypad monitor as defined in claim 12 further including means for generating a unique multiple-bit code identifying which of said plurality of switches has a changed condition in response to receiving said clock signals.

14. A keypad monitor as defined in claim 13 wherein said multiple-bit code is a six-bit code.

15. A keypad monitor as defined in claim 12 wherein said change in said condition of said plurality of switches is closure of said plurality of switches.

5

15

20

25

16. A keypad monitor as defined in claim 12 wherein said keypad monitoring means further includes a first and a second plurality of conductors and wherein each switch of said plurality of switches is coupled between a respective different pair of first and second conductors, wherein said keypad monitoring means includes a first plurality of inputs coupled to said first conductors and a second plurality of inputs coupled to said second conductors, and wherein said keypad monitoring means are arranged for monitoring only said first or said second plurality of inputs for providing said first output signal.

17. A keypad monitor as defined in claim 16 wherein said first plurality of conductors comprises row conductors, wherein said second plurality of conductors comprises column conductors, and wherein said keypad monitoring means are arranged for monitoring only said first plurality of inputs for providing said first output signal.

18. A keypad monitor as defined in claim 16 wherein said keypad monitoring means further includes an OR gate having a plurality of inputs coupled to said first plurality of inputs and an output for providing said first output signal.

* * * * *

30

35

40

45

50

55

60

65



US005235635C1

(12) **EX PARTE REEXAMINATION CERTIFICATE (8253rd)**
United States Patent
Gulick

(10) **Number:** **US 5,235,635 C1**
(45) **Certificate Issued:** **May 24, 2011**

(54) **KEYPAD MONITOR WITH KEYPAD ACTIVITY-BASED ACTIVATION**

(75) Inventor: **Dale E. Gulick**, Austin, TX (US)

(73) Assignee: **Norman IP Holdings LLC**, Tyler, TX (US)

Reexamination Request:
No. 90/010,850, Feb. 4, 2010

Reexamination Certificate for:
Patent No.: **5,235,635**
Issued: **Aug. 10, 1993**
Appl. No.: **07/589,090**
Filed: **Sep. 27, 1990**

(51) **Int. Cl.**
H04M 1/26 (2006.01)

(52) **U.S. Cl.** **379/368; 379/362; 379/364; 379/365**

(58) **Field of Classification Search** None
See application file for complete search history.

(56) **References Cited**

U.S. PATENT DOCUMENTS

4,775,928 A 10/1988 Kendall et al.
5,148,380 A 9/1992 Lin et al.
5,355,503 A 10/1994 Soffel et al.

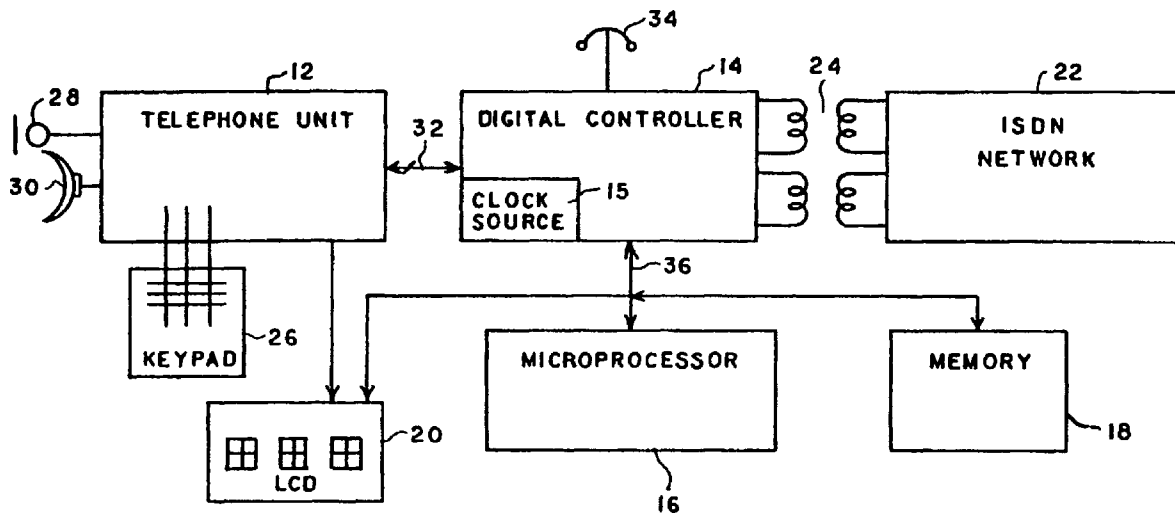
OTHER PUBLICATIONS

AP-252 Application Note, "Designing with the 80C51BH," Intel Corp., Match, 1985.

Primary Examiner—Charles Craver

(57) **ABSTRACT**

A keypad monitor which monitors the condition of a plurality of switches of a keypad is activated in response to receiving clock signals from an external clock source and is deactivated in the absence of the clock signals. The keypad monitor includes a key switch monitor which detects the closure of any one of the keypad switches to provide a first output signal, a clock detector for detecting the absence of the clock signals to provide a second output signal and any key down output logic which is responsive to the first and second output signals for providing an activation signal. The activation signal causes the external clock source to provide the keypad monitor with the clock signals for activating the keypad monitor.



1
EX PARTE
REEXAMINATION CERTIFICATE
ISSUED UNDER 35 U.S.C. 307

THE PATENT IS HEREBY AMENDED AS
INDICATED BELOW.

2
AS A RESULT OF REEXAMINATION, IT HAS BEEN
DETERMINED THAT:
Claims **12-18** are cancelled.
5 Claims **1-11** were not reexamined.

* * * * *